STANDARDS CHANGES CATALOG (SCC)

SCC NUMBER: SCC #140

CHANGE PROPOSAL TITLE: Segmentation/Reassembly (S/R) Header:

Byte Order of Transmission

Clarification

ORIGINATOR and ADDRESS: Steve Turczyn

Army Representative

stephen.turczyn@mail1.monmouth.army.mil

732-532-8453; DSN 992-8453

Ketan Patel

Marine Corps Representative

patelkk@mctssa.usmc.mil

760-725-2374; DSN: 365-2374

Bob Lynch

CNRWG Facilitator

robert.j.lynch@us.army.mil
732-532-7582; DSN: 992-7582

ORIGINATOR'S INTERNAL NUMBER:

AFFECTED DOCUMENT: MIL-STD-2045-47001C

PRECEDENCE: Priority

RECOMMENDATIONS: Required for Army - Marine Corps

interoperability.

RECORD OF PROCESSING

DATE:			ACTION:
03	Mar	03	Proposal
07	Mar	03	Work Item
07	Apr	03	R2; Draft

14 May 03 R3; Approved for MIL-STD-045-47001C

- 1. STATEMENT OF THE PROBLEM: The byte order of S/R header is ambiguous. The interface between the Army's Maneuver Control System (MCS) and the Marine Corps' Command & Control Personal Computer (C2PC) has raised the issue on the proper byte order of transmission of S/R header information. The CNRWG has been very careful in the standard to show byte order in every other case to avoid ambiguity. S/R escaped notice when examples with actual values were added.
- 2. PROBLEM ANALYSIS: Appendix C of MIL-STD-2045-47001 describes the S/R Protocol. This is a DoD unique protocol for segmenting 47001 messages larger than maximum segment size. The header itself is shown in standard User Datagram Protocol/Internet Protocol (UDP/IP) format where the information is shown in 32 bit blocks. The intent of MIL-STD-2045-47001 is that for transmission purposes the S/R Header be treated like standard UDP/IP headers. Once the header is created it is treated as a series of octets. The octets are therefore transmitted in Network Byte Order or most significant byte first. The underlying layers dictate actual bit order. If the S/R is transmitted under MIL-STD-188-220 then the bit order is Least Significant Bit (LSB) of the octet first.
- 3. PROPOSED SOLUTION: See attached pages.
- 4. ALTERNATIVE SOLUTIONS: None.
- 5. SYSTEM CHANGES REQUIRED: None.
- 6. CONFIGURATION ITEM DOCUMENTATION CHANGES: MIL-STD-2045-47001C.

 Note: If one is using S/R in MIL-STD-2045-47001B then the example in MIL-STD-2045-47001C should be used for byte order clarification. Since MIL-STD-2045-47001B will not be updated, a reference stating the first sentence in the above Note will be placed on the CNRWG website and on DoD's ASSIST database in the Document Part Description for downloading the PDF file for Revision B.
- 7. <u>IMPACT ON INTEROPERABILITY</u>: Improves interoperability because the S/R header byte order was previously ambiguous.
- 8. IMPACT ON RELATED DOCUMENTS: None.
- 9. IMPLEMENTATION DATES: When approved for C2PC.

- 10. <u>OTHER CONSIDERATIONS</u>: None.
- 11. <u>REFERENCES</u>: None.
- 12. Trouble Reports (TRs) ADDRESSED IN THIS SCC: None.

C.4.1 Common S/R Header.

Figure 7 depicts the S/R header that precedes all PDUs defined in this appendix. <u>Each octet is marked to show MSB and LSB.</u>

MSB	LSB	MSB	LSB	MSB	LSB	MSB	LSB	
0	7	8	<u>15</u>	<u>16</u>	23	<u>24</u>	<u>31</u>	
Source Port				<u>Destination Port</u>				
Type	H	<u>LEN</u>	P/F Serial Number					

Figure 7. Segmentation/Reassembly header

	Source Port		Destination Port
Type	HLEN	P/ F	Serial Number

Figure 7. Segmentation/Reassembly header

MSB	<u>LSB</u>	MSB	<u>LSB</u>	MSB	<u>LSB</u>	MSB	<u>LSB</u>	
0	7	8	<u>15</u>	<u>16</u>	23	<u>24</u>	31	
Source Port				<u>Destination Port</u>				
<u>Type</u>	<u>H</u>	<u>LEN</u>	<u>P/F</u>		Serial 1	<u>Number</u>		
Segment Number				Last Segment Number				
ĺ	Data Portion							

Type = 000 or 010

Figure 8. Data segment

	Source Port		Destination Port
Type	HLEN	P/F	Serial Number
	Segment Number		Last Segment Number
		Data F	Portion

Type = 000 or 010

Figure 8. Data segment

MSB	LSB	MSB	LSB	MSB LSB MSB LSI			<u>LSB</u>		
0	7	8	<u>15</u>	<u>16 23 24 3</u>			31		
	Source Port				<u>Destination Port</u>				
Type <u>HLEN</u> <u>P/F</u>				Serial Number					
Starting Segment No.			Bit	<u>Map</u>	Pad	<u>ded</u>			

 $\underline{\text{Type}} = 100$

Figure 9. Partial acknowledgement segment

	Source Por	ŧ	Destination Port			
Type	HLEN	P/ F	Serial Number			
S	tarting Segme	nt No.	Bit Map	Padded		

 $\overline{\text{Type}} = \overline{100}$

Figure 9. Partial acknowledgement segment

MSB	LSB	MSB	LSB	MSB	LSB	MSB	LSB	
0	7	8	<u>15</u>	<u>16</u>	23	<u>24</u>	31	
Source Port				<u>Destination Port</u>				
<u>Type</u>	<u>H</u>	<u>LEN</u>	P/F	Serial Number				

 $\overline{\text{Type} = 110}$

Figure 10. Complete acknowledgement segment

	Source Port	÷	Destination Port
Type	HLEN	P/F	Serial Number

Type = 110

Figure 10. Complete acknowledgement segment

MSB	LSB	MSB	LSB	MSB	LSB	MSB	LSB
0	7	8	<u>15</u>	<u>16</u>	23	<u>24</u>	31
Source Port					<u>Destinat</u>	ion Port	
Type	Н	LEN	P/F	Serial Number			

Type = 001 or 101

Figure 11. Abort request/confirm segment

	Source Port	;	Destination Port
Type	HLEN	P/F	Serial Number

Type = 001 or 101

Figure 11. Abort request/confirm segment

MSB	LSB	MSB	LSB	MSB	LSB	MSB	LSB		
0	7	8	<u>15</u>	<u>16</u>	23	<u>24</u>	<u>31</u>		
	Source Port				<u>Destination Port</u>				
Type <u>HLEN</u> <u>P/F</u>				Serial Number					
<u>Last Segment Number</u>					<u>Pad</u>	<u>ded</u>			

 $\underline{\text{Type}} = 011$

Figure 12. Acknowledgement request segment

	Source Por	ŧ	Destination Port		
Type	HLEN	P/F	Serial Number		
Last Sent Segment No.			Padded		

Tvpe = 011

Figure 12. Acknowledgement request segment

C4.8 Example.

The construction of the S/R header is illustrated by the example in TABLE SCC-140. The first four columns of the table provide a description of each field in the example, the field length in bits, and the value of the field in both decimal and binary representations. The last four columns show the physical encoding of the S/R header. In the fifth column, Field Fragments, the bits of each field are placed in octets. The bit(s) of each field are positioned in an octet such that the LSB of the field is positioned in the least significant unencoded bit of the octet, the next LSB of the field is placed in the next least significant unencoded bit of the octet, and repeated until all of the bits of the field have been encoded. When an octet is filled before all the bits of a field are encoded, the process is continued by encoding the next octet with the remaining bits of the field. This field/octet encoding procedure is performed starting with the first field and octet, and repeated for each successive field and individual octet, in order, until the encoding is completed. When a field has groups, the field encoding procedure is performed starting with the first group, and repeated for each successive group and individual octet, in order, until the encoding of the field is completed. The Target Number field illustrates the encoding of a field with groups. Note the LSB of a field or octet is defined as the bit having the weight of 20 when the field or octet is represented as a numeric value. X's are used to identify bits that are not associated with the field being encoded. The sixth column, Octet Value - Binary, assembles the bits contributed by successive fields into complete octets, represented in binary. The seventh column, Octet Value -Hexadecimal, represents the octet value in hexadecimal. The last column, Octet Number, numbers the octets from first to last starting with 0.

Each S/R header is individually encoded. For this example, the Source has a value of 5000, the Destination has a value of 1581, the Type equals 2, HELEN equals 3, P/F equals 1, Serial Number has a value of 16000, Segment Number has a value of 260 and the Last Segment Number equals 300.

TABLE SCC-140. Example construction of S/R header data

FIELD OCTET BUFFER/STREAM								
TITLE	LENGTH	VALUE	VALUE		FIELD	OCTET	OCTET	OCTET
					FRAGMENTS		VALUE	
	(Bits)	(Dec)	(Binary)			(Binary)	(Hex)	<u> </u>
	•	•						
			MSB	LSB	MSB LSB	MSB LSB		
			2 ⁿ	<u>LSB</u>	2 ⁷ 2 ⁰	$\frac{1000}{2^7}$ $\frac{100}{2^0}$		
Source Port	<u>16</u>	<u>5000</u>	0001001110001000		<u>00010011</u>	00010011	<u>13</u>	<u>0</u>
					<u>10001000</u>	10001000	<u>88</u>	1
Destination Port	<u>16</u>	<u>1581</u>	0010110100000110		<u>00000110</u>	00000110	<u>06</u>	2
					<u>00101101</u>	<u>00101101</u>	<u>2D</u>	<u>3</u>
<u>Type</u>	<u>3</u>	<u>2</u>	<u>010</u>		010XXXXX			
HLEN	<u>12</u>	<u>3</u>	00000000011		XXX00000	01000000	<u>40</u>	4
					0000011X			
P/F	1	<u>1</u>	1		XXXXXXX1	00000111	<u>07</u>	<u>5</u>
Serial Number	<u>16</u>	<u>16000</u>	0011111010000000		<u>00111110</u>	<u>00111110</u>	<u>3E</u>	<u>6</u>
					10000000	10000000	<u>80</u>	<u>7</u>
Segment Number	<u>16</u>	<u>260</u>	0000000100000100		0000001	0000001	<u>01</u>	<u>8</u>
					00000100	00000100	<u>04</u>	9
Last Segment Number	<u>16</u>	<u>300</u>	0000000100101100		0000001	0000001	<u>01</u>	<u>10</u>
					00101100	00101100	<u>2C</u>	<u>11</u>
I	1	1	1		II	l		